


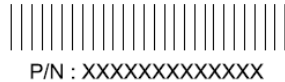


<b>Test Report No.:</b> PV181122E10	
<b>Client</b>	
Name :	Ablerex Electronics Co., Ltd.
Address :	1F, No. 3, Lane 7, Paokao Rd., Hsintien, New Taipei City, Taiwan
<b>Test Item :</b>	Grid-tied photovoltaic inverter
<b>Identification :</b>	ES 52000HC, ES 52000HC-S, ES 52000HC-I, ES 52000HC-SI
<b>Testing laboratory</b>	
Name :	Bureau Veritas Consumer Products Services (H.K.) Ltd., Taoyuan Branch
Address :	No. 47-2, 14th Ling, Chia Pau Vil., Lin Kou Dist., New Taipei City, Taiwan
<b>Test specification</b>	
<b>Standard :</b>	IEEE 519:2014
<b>Test Result :</b>	The test item passed.
<b>Prepared By :</b>	
 Signature Dino Kao Senior Engineer	2018-12-25 Date
<b>Approved By:</b>	
 Signature Edward Chiueh Technical Manager	2018-12-25 Date
<b>Other Aspects:</b>	
The completed test report includes the following documents: IEEE 519:2014 report (29 pages)	
<p>This report is for your exclusive use. Any copying or replication of this report to or for any other person or entity, or use of our name or trademark, is permitted only with our prior written permission. This report sets forth our findings solely with respect to the test samples identified herein. The results set forth in this report are not indicative or representative of the quality or characteristics of the lot from which a test sample was taken or any similar or identical product unless specifically and expressly noted. Our report includes all of the tests requested by you and the results thereof based upon the information that you provided to us. You have 60 days from date of issuance of this report to notify us of any material error or omission caused by our negligence, provided, however, that such notice shall be in writing and shall specifically address the issue you wish to raise. A failure to raise such issue within the prescribed time shall constitute your unqualified acceptance of the completeness of this report, the tests conducted and the correctness of the report contents. Unless specific mention, the uncertainty of measurement has been explicitly taken into account to declare the compliance or non-compliance to the specification</p>	

<b>TEST REPORT</b>	
<b>IEEE 519:2014</b>	
<b>IEEE Recommended Practice and Requirements for Harmonic Control in Electric Power Systems</b>	
<b>Report reference No.</b> .....	PV181122E10
Tested by (printed name and signature) .....	See cover sheet
Approved by (printed name and signature) .....	See cover sheet
Date of issue .....	2018-12-25
<b>Testing Laboratory Name</b> .....	
Bureau Veritas Consumer Products Services (H.K.) Ltd., Taoyuan Branch	
Address.....	No. 47-2, 14th Ling, Chia Pau Vil., Lin Kou Dist., New Taipei City, Taiwan
Testing location.....	Bureau Veritas Consumer Product Services Limited, Taoyuan Branch
Address.....	No. 19, Hwa Ya 2nd Rd., Wen Hwa Tsuen, Kwei Shan Hsiang, Taoyuan Hsien 333, Taiwan
<b>Applicant's Name</b> .....	Ablerex Electronics Co., Ltd.
Address .....	1F, No. 3, Lane 7, Paokao Rd., Hsintien, New Taipei City, Taiwan
<b>Test specification</b>	
Standard .....	IEEE 519:2014
Non-standard test method .....	None
<b>Test Report Form No.</b> .....	IEEE 519:2014_A
Master TRF .....	Bureau Veritas Consumer Product Services GmbH
Copyright © Bureau Veritas Consumer Products Services (H.K.) Ltd., Taoyuan Branch	
<b>Test item description</b> .....	Solar Inverter
Trademark .....	
Model / Type .....	ES 52000HC, ES 52000HC-S, ES 52000HC-I, ES 52000HC-SI
<b>Ratings</b> .....	
PV Array Input DC voltage range [V]:	200-1000 Vdc
PV Array Input DC current [A] .....	26 A x 4
Output AC voltage [V] .....	380 Vac, 60Hz
Output AC current [A] .....	78.8 A x 3
Output power [kVA] .....	52 kVA

Copy of marking plate

太陽能變流器  
型號: ES 52000HC



直流輸入  
電壓: 200~1,000 Vdc  
電流: 26 Amp x 4  
最大功率追蹤範圍: 200~1000 Vdc

額定交流輸出  
功率: 52 kVA  
功率因數: 0.8超前~0.8落後  
電壓: 380 Vac  
電流: 78.8 Amp x 3  
頻率: 60 Hz



保護等級: I  
侵入保護: IP65

盈正豫順電子股份有限公司  
ABLEREX ELECTRONICS CO., LTD.

S/N : MA3C013000001

太陽能變流器  
型號: ES 52000HC-S



直流輸入  
電壓: 200~1,000 Vdc  
電流: 26 Amp x 4  
最大功率追蹤範圍: 200~1000 Vdc

額定交流輸出  
功率: 52 kVA  
功率因數: 0.8超前~0.8落後  
電壓: 380 Vac  
電流: 78.8 Amp x 3  
頻率: 60 Hz



保護等級: I  
侵入保護: IP65

盈正豫順電子股份有限公司  
ABLEREX ELECTRONICS CO., LTD.

S/N : MA3C013000001

太陽能變流器  
型號: ES 52000HC-I



直流輸入  
電壓: 200~1,000 Vdc  
電流: 26 Amp x 4  
最大功率追蹤範圍: 200~1000 Vdc

額定交流輸出  
功率: 52 kVA  
功率因數: 0.8超前~0.8落後  
電壓: 380 Vac  
電流: 78.8 Amp x 3  
頻率: 60 Hz

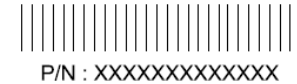


保護等級: I  
侵入保護: IP65

盈正豫順電子股份有限公司  
ABLEREX ELECTRONICS CO., LTD.

S/N : MA3C013000001

太陽能變流器  
型號: ES 52000HC-SI



直流輸入  
電壓: 200~1,000 Vdc  
電流: 26 Amp x 4  
最大功率追蹤範圍: 200~1000 Vdc

額定交流輸出  
功率: 52 kVA  
功率因數: 0.8超前~0.8落後  
電壓: 380 Vac  
電流: 78.8 Amp x 3  
頻率: 60 Hz



保護等級: I  
侵入保護: IP65

盈正豫順電子股份有限公司  
ABLEREX ELECTRONICS CO., LTD.

S/N : MA3C013000001

### History Sheet:

Name	Date	Comment	Revision
Dino Kao	2018-12-25	Initial report was written	Empty

### Address of the manufacturer sites:

**1) Ablere Electronics (SUZHOU) Co., Ltd.**

No. 36 Wang Wu Road, Wu Zhong District Suzhou, 215128, P.R. China

**2) Ablere Electronics Co., Ltd.**

No. 1-1, Gongye Rd., Pingtung City, Pingtung Country 90049, Taiwan

<b>Particulars:</b>											
Equipment mobility .....	Permanent connection										
Operating condition .....	Continuous										
Class of equipment .....	Class I										
<b>Test case verdicts:</b>											
Test case does not apply to the test object .....	<b>N/A</b>										
Test item does meet the requirement .....	<b>P(ass)</b>										
Test item does not meet the requirement .....	<b>F(ail)</b>										
<b>Testing:</b>											
Date of receipt of test item .....	2018-12-04										
Date(s) of performance of test .....	2018-12-04										
<p><b>General remarks:</b></p> <p>The test result presented in this report relate only to the object(s) tested.          This report shall not be reproduced, except in full, without the written approval of the applicant.          Throughout this report a comma is used as the decimal separator.</p> <p>All tests are performed on model ES 52000HC.</p> <p><b>Model Differences:</b></p> <p>The models are identical in Hardware and Software except followings.</p> <table border="1"> <thead> <tr> <th>Model</th> <th>Description</th> </tr> </thead> <tbody> <tr> <td>ES 52000HC</td> <td>Standard model</td> </tr> <tr> <td>ES 52000HC-S</td> <td>Include SPD</td> </tr> <tr> <td>ES 52000HC-I</td> <td>Include ICM2</td> </tr> <tr> <td>ES 52000HC-SI</td> <td>Include SPD and ICM2</td> </tr> </tbody> </table> <p><b>This Test Report consists of the following documents:</b></p> <ol style="list-style-type: none"> <li>1. Test Results</li> <li>2. Annex 1: Pictures of the unit</li> <li>3. Annex 2: Test equipment list</li> </ol>		Model	Description	ES 52000HC	Standard model	ES 52000HC-S	Include SPD	ES 52000HC-I	Include ICM2	ES 52000HC-SI	Include SPD and ICM2
Model	Description										
ES 52000HC	Standard model										
ES 52000HC-S	Include SPD										
ES 52000HC-I	Include ICM2										
ES 52000HC-SI	Include SPD and ICM2										

4.5 (B1) Harmonic Current Limit Test				P
Test Sample: ES 52000HC				
Watts [kW]		17,42		
VA [kVA]		17,43		
Vrms [V]		221,34		
Arms [A]		78,73		
Power Factor		0,9994		
THD [%]		3,64		
Harmonics	Current Magnitude (A)	% of Fundamental	Phase	Harmonic Current Limits (%)
1st	78,73	—	R	--
2nd	0,56	0,71	R	1%
3rd	0,47	0,60	R	4%
4th	0,37	0,47	R	1%
5th	2,51	3,19	R	4%
6th	0,29	0,37	R	1%
7th	1,49	1,89	R	4%
8th	0,12	0,15	R	1%
9th	0,26	0,33	R	4%
10th	0,07	0,09	R	1%
11th	0,51	0,65	R	2%
12th	0,06	0,07	R	0,5%
13th	0,32	0,41	R	2%
14th	0,03	0,04	R	0,5%
15th	0,08	0,10	R	2%
16th	0,05	0,06	R	0,5%
17th	0,14	0,18	R	1,5%
18th	0,02	0,03	R	0,375%
19th	0,11	0,14	R	1,5%
20th	0,03	0,04	R	0,375%
21th	0,03	0,04	R	1,5%
22th	0,02	0,03	R	0,375%
23th	0,09	0,12	R	0,6%
24th	0,02	0,03	R	0,15%
25th	0,06	0,08	R	0,6%
26th	0,02	0,03	R	0,15%
27th	0,02	0,03	R	0,6%
28th	0,02	0,02	R	0,15%
29th	0,06	0,07	R	0,6%
30th	0,02	0,03	R	0,15%
31th	0,03	0,04	R	0,6%
32th	0,02	0,03	R	0,15%
33th	0,02	0,02	R	0,6%
34th	0,02	0,03	R	0,15%
35th	0,03	0,04	R	0,3%
36th	0,02	0,02	R	0,075%
37th	0,02	0,03	R	0,3%
38th	0,02	0,03	R	0,075%
39th	0,02	0,02	R	0,3%
40th	0,02	0,03	R	0,075%

Test Sample: ES 52000HC				
Watts [kW]		17,42		
VA [kVA]		17,43		
Vrms [V]		221,34		
Arms [A]		78,73		
Power Factor		0,9994		
THD [%]		3,64		
Harmonics	Current Magnitude (A)	% of Fundamental	Phase	Harmonic Current Limits (%)
41th	0,03	0,04	R	0,3%
42th	0,04	0,05	R	0,075%
43th	0,05	0,06	R	0,3%
44th	0,06	0,07	R	0,075%
45th	0,03	0,04	R	0,3%
46th	0,03	0,04	R	0,075%
47th	0,02	0,02	R	0,3%
48th	0,02	0,02	R	0,075%
49th	0,02	0,03	R	0,3%
50th	0,02	0,02	R	0,075%
Note:				
$I_{sc}/I_L = 1$				
Ref. to Table 2 of the IEEE 519:2014				

Test Sample: ES 52000HC				
Watts [kW]		17,25		
VA [kVA]		17,27		
Vrms [V]		221,10		
Arms [A]		78,09		
Power Factor		0,9990		
THD [%]		3,42		
Harmonics	Current Magnitude (A)	% of Fundamental	Phase	Harmonic Current Limits (%)
1st	78,09	—	S	--
2nd	0,60	0,77	S	1%
3rd	0,27	0,35	S	4%
4th	0,30	0,38	S	1%
5th	2,34	3,00	S	4%
6th	0,13	0,17	S	1%
7th	1,30	1,67	S	4%
8th	0,10	0,13	S	1%
9th	0,15	0,19	S	4%
10th	0,09	0,11	S	1%
11th	0,56	0,72	S	2%
12th	0,08	0,10	S	0,5%
13th	0,34	0,43	S	2%
14th	0,03	0,04	S	0,5%
15th	0,05	0,07	S	2%
16th	0,05	0,06	S	0,5%
17th	0,17	0,22	S	1,5%
18th	0,04	0,05	S	0,375%

Test Sample: ES 52000HC				
Watts [kW]		17,25		
VA [kVA]		17,27		
Vrms [V]		221,10		
Arms [A]		78,09		
Power Factor		0,9990		
THD [%]		3,42		
Harmonics	Current Magnitude (A)	% of Fundamental	Phase	Harmonic Current Limits (%)
19th	0,12	0,15	S	1,5%
20th	0,03	0,04	S	0,375%
21th	0,04	0,05	S	1,5%
22th	0,03	0,04	S	0,375%
23th	0,10	0,13	S	0,6%
24th	0,03	0,04	S	0,15%
25th	0,06	0,08	S	0,6%
26th	0,02	0,03	S	0,15%
27th	0,02	0,03	S	0,6%
28th	0,02	0,02	S	0,15%
29th	0,06	0,08	S	0,6%
30th	0,02	0,03	S	0,15%
31th	0,03	0,04	S	0,6%
32th	0,02	0,02	S	0,15%
33th	0,02	0,02	S	0,6%
34th	0,02	0,02	S	0,15%
35th	0,03	0,04	S	0,3%
36th	0,02	0,02	S	0,075%
37th	0,02	0,03	S	0,3%
38th	0,02	0,03	S	0,075%
39th	0,02	0,03	S	0,3%
40th	0,03	0,04	S	0,075%
41th	0,04	0,05	S	0,3%
42th	0,05	0,06	S	0,075%
43th	0,05	0,06	S	0,3%
44th	0,03	0,04	S	0,075%
45th	0,02	0,03	S	0,3%
46th	0,03	0,04	S	0,075%
47th	0,02	0,02	S	0,3%
48th	0,02	0,02	S	0,075%
49th	0,02	0,02	S	0,3%
50th	0,02	0,02	S	0,075%
Note: $I_{sc}/I_L = 1$ Ref. to Table 2 of the IEEE 519:2014				



Test Sample: ES 52000HC				
Watts [kW]		17,78		
VA [kVA]		17,79		
Vrms [V]		221,18		
Arms [A]		80,42		
Power Factor		0,9993		
THD [%]		3,39		
Harmonics	Current Magnitude (A)	% of Fundamental	Phase	Harmonic Current Limits (%)
1st	80,42	—	T	--
2nd	0,27	0,34	T	1%
3rd	0,31	0,38	T	4%
4th	0,35	0,43	T	1%
5th	2,12	2,64	T	4%
6th	0,19	0,24	T	1%
7th	1,59	1,98	T	4%
8th	0,09	0,11	T	1%
9th	0,14	0,18	T	4%
10th	0,06	0,08	T	1%
11th	0,47	0,58	T	2%
12th	0,06	0,07	T	0,5%
13th	0,43	0,53	T	2%
14th	0,03	0,04	T	0,5%
15th	0,05	0,06	T	2%
16th	0,03	0,04	T	0,5%
17th	0,16	0,20	T	1,5%
18th	0,03	0,04	T	0,375%
19th	0,14	0,18	T	1,5%
20th	0,03	0,04	T	0,375%
21th	0,02	0,03	T	1,5%
22th	0,02	0,03	T	0,375%
23th	0,10	0,12	T	0,6%
24th	0,02	0,03	T	0,15%
25th	0,08	0,10	T	0,6%
26th	0,03	0,04	T	0,15%
27th	0,02	0,03	T	0,6%
28th	0,02	0,02	T	0,15%
29th	0,06	0,07	T	0,6%
30th	0,02	0,02	T	0,15%
31th	0,04	0,05	T	0,6%
32th	0,02	0,03	T	0,15%
33th	0,02	0,02	T	0,6%
34th	0,02	0,02	T	0,15%
35th	0,03	0,04	T	0,3%
36th	0,02	0,02	T	0,075%
37th	0,03	0,04	T	0,3%
38th	0,02	0,03	T	0,075%
39th	0,02	0,03	T	0,3%
40th	0,02	0,03	T	0,075%
41th	0,02	0,03	T	0,3%
42th	0,03	0,04	T	0,075%
43th	0,04	0,05	T	0,3%

Test Sample: ES 52000HC				
Watts [kW]		17,78		
VA [kVA]		17,79		
Vrms [V]		221,18		
Arms [A]		80,42		
Power Factor		0,9993		
THD [%]		3,39		
Harmonics	Current Magnitude (A)	% of Fundamental	Phase	Harmonic Current Limits (%)
44th	0,06	0,07	T	0,075%
45th	0,03	0,04	T	0,3%
46th	0,02	0,03	T	0,075%
47th	0,02	0,02	T	0,3%
48th	0,02	0,02	T	0,075%
49th	0,02	0,02	T	0,3%
50th	0,02	0,02	T	0,075%
Note: $I_{sc}/I_L = 1$ Ref. to Table 2 of the IEEE 519:2014				

4.5 (B1) Harmonic Voltage Limit Test				P
Test conditions:				
THD50			1,07	
Harmonics	Voltage Magnitude (V)	% of Fundamental	Phase	Harmonic Limits of Test Voltage (%)
1st	221,34	--	R	--
2nd	0,18	0,08	R	3%
3rd	1,11	0,5	R	3%
4th	0,22	0,1	R	3%
5th	1,51	0,68	R	3%
6th	0,24	0,11	R	3%
7th	1,33	0,6	R	3%
8th	0,13	0,06	R	3%
9th	0,33	0,15	R	3%
10th	0,09	0,04	R	3%
11th	0,75	0,34	R	3%
12th	0,11	0,05	R	3%
13th	0,60	0,27	R	3%
14th	0,07	0,03	R	3%
15th	0,22	0,1	R	3%
16th	0,07	0,03	R	3%
17th	0,33	0,15	R	3%
18th	0,07	0,03	R	3%
19th	0,24	0,11	R	3%
20th	0,04	0,02	R	3%
21th	0,11	0,05	R	3%
22th	0,07	0,03	R	3%
23th	0,15	0,07	R	3%
24th	0,04	0,02	R	3%
25th	0,11	0,05	R	3%
26th	0,04	0,02	R	3%
27th	0,04	0,02	R	3%
28th	0,04	0,02	R	3%
29th	0,11	0,05	R	3%
30th	0,04	0,02	R	3%
31th	0,09	0,04	R	3%
32th	0,04	0,02	R	3%
33th	0,04	0,02	R	3%
34th	0,04	0,02	R	3%
35th	0,04	0,02	R	3%
36th	0,04	0,02	R	3%
37th	0,04	0,02	R	3%
38th	0,07	0,03	R	3%
39th	0,04	0,02	R	3%
40th	0,07	0,03	R	3%
41th	0,04	0,02	R	3%
42th	0,09	0,04	R	3%
43th	0,09	0,04	R	3%
44th	0,11	0,05	R	3%
45th	0,09	0,04	R	3%
46th	0,09	0,04	R	3%

Test conditions:				
THD50			1,07	
Harmonics	Voltage Magnitude (V)	% of Fundamental	Phase	Harmonic Limits of Test Voltage (%)
47th	0,07	0,03	R	3%
48th	0,07	0,03	R	3%
49th	0,07	0,03	R	3%
50th	0,04	0,02	R	3%
Note:				

Test conditions:				
THD50			0,99	
Harmonics	Voltage Magnitude (V)	% of Fundamental	Phase	Harmonic Limits of Test Voltage (%)
1st	221,10	--	S	--
2nd	0,20	0,09	S	3%
3rd	0,95	0,43	S	3%
4th	0,18	0,08	S	3%
5th	1,35	0,61	S	3%
6th	0,11	0,05	S	3%
7th	1,13	0,51	S	3%
8th	0,13	0,06	S	3%
9th	0,13	0,06	S	3%
10th	0,13	0,06	S	3%
11th	0,80	0,36	S	3%
12th	0,09	0,04	S	3%
13th	0,60	0,27	S	3%
14th	0,04	0,02	S	3%
15th	0,20	0,09	S	3%
16th	0,07	0,03	S	3%
17th	0,38	0,17	S	3%
18th	0,07	0,03	S	3%
19th	0,27	0,12	S	3%
20th	0,04	0,02	S	3%
21th	0,09	0,04	S	3%
22th	0,04	0,02	S	3%
23th	0,18	0,08	S	3%
24th	0,04	0,02	S	3%
25th	0,11	0,05	S	3%
26th	0,04	0,02	S	3%
27th	0,04	0,02	S	3%
28th	0,04	0,02	S	3%
29th	0,13	0,06	S	3%
30th	0,04	0,02	S	3%
31th	0,09	0,04	S	3%
32th	0,02	0,01	S	3%
33th	0,02	0,01	S	3%
34th	0,04	0,02	S	3%
35th	0,07	0,03	S	3%
36th	0,04	0,02	S	3%
37th	0,04	0,02	S	3%

Test conditions:				
THD50			0,99	
Harmonics	Voltage Magnitude (V)	% of Fundamental	Phase	Harmonic Limits of Test Voltage (%)
38th	0,07	0,03	S	3%
39th	0,07	0,03	S	3%
40th	0,09	0,04	S	3%
41th	0,07	0,03	S	3%
42th	0,11	0,05	S	3%
43th	0,11	0,05	S	3%
44th	0,09	0,04	S	3%
45th	0,07	0,03	S	3%
46th	0,07	0,03	S	3%
47th	0,04	0,02	S	3%
48th	0,04	0,02	S	3%
49th	0,07	0,03	S	3%
50th	0,04	0,02	S	3%
Note:				

Test conditions:				
THD50			1,03	
Harmonics	Voltage Magnitude (V)	% of Fundamental	Phase	Harmonic Limits of Test Voltage (%)
1st	221,18	--	T	--
2nd	0,11	0,05	T	3%
3rd	0,95	0,43	T	3%
4th	0,22	0,1	T	3%
5th	1,19	0,54	T	3%
6th	0,18	0,08	T	3%
7th	1,39	0,63	T	3%
8th	0,09	0,04	T	3%
9th	0,15	0,07	T	3%
10th	0,07	0,03	T	3%
11th	0,69	0,31	T	3%
12th	0,09	0,04	T	3%
13th	0,71	0,32	T	3%
14th	0,04	0,02	T	3%
15th	0,24	0,11	T	3%
16th	0,07	0,03	T	3%
17th	0,38	0,17	T	3%
18th	0,04	0,02	T	3%
19th	0,31	0,14	T	3%
20th	0,04	0,02	T	3%
21th	0,11	0,05	T	3%
22th	0,04	0,02	T	3%
23th	0,18	0,08	T	3%
24th	0,04	0,02	T	3%
25th	0,15	0,07	T	3%
26th	0,04	0,02	T	3%
27th	0,04	0,02	T	3%
28th	0,04	0,02	T	3%



Test conditions:				
THD50			1,03	
Harmonics	Voltage Magnitude (V)	% of Fundamental	Phase	Harmonic Limits of Test Voltage (%)
29th	0,13	0,06	T	3%
30th	0,04	0,02	T	3%
31th	0,09	0,04	T	3%
32th	0,04	0,02	T	3%
33th	0,04	0,02	T	3%
34th	0,04	0,02	T	3%
35th	0,07	0,03	T	3%
36th	0,04	0,02	T	3%
37th	0,04	0,02	T	3%
38th	0,07	0,03	T	3%
39th	0,07	0,03	T	3%
40th	0,07	0,03	T	3%
41th	0,07	0,03	T	3%
42th	0,09	0,04	T	3%
43th	0,11	0,05	T	3%
44th	0,13	0,06	T	3%
45th	0,09	0,04	T	3%
46th	0,07	0,03	T	3%
47th	0,07	0,03	T	3%
48th	0,04	0,02	T	3%
49th	0,04	0,02	T	3%
50th	0,04	0,02	T	3%
Note:				

# **Annex 1**

## **Pictures of the unit**

**Inverter  
Enclosure front**



**Enclosure rear side**

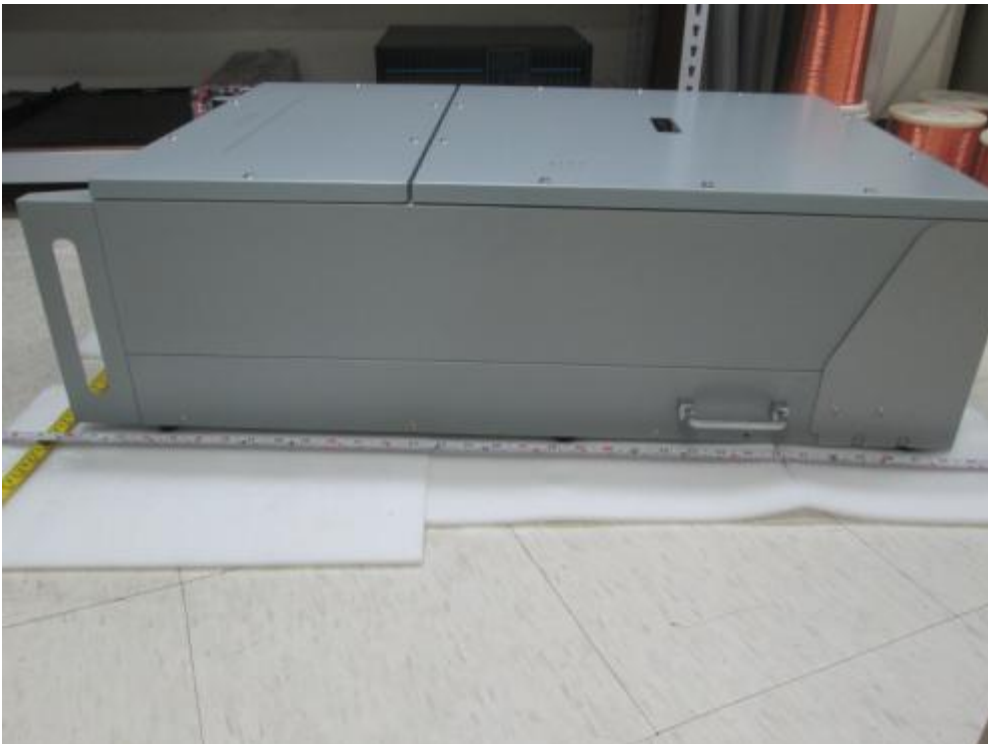




**Enclosure Left side**



**Enclosure right side**



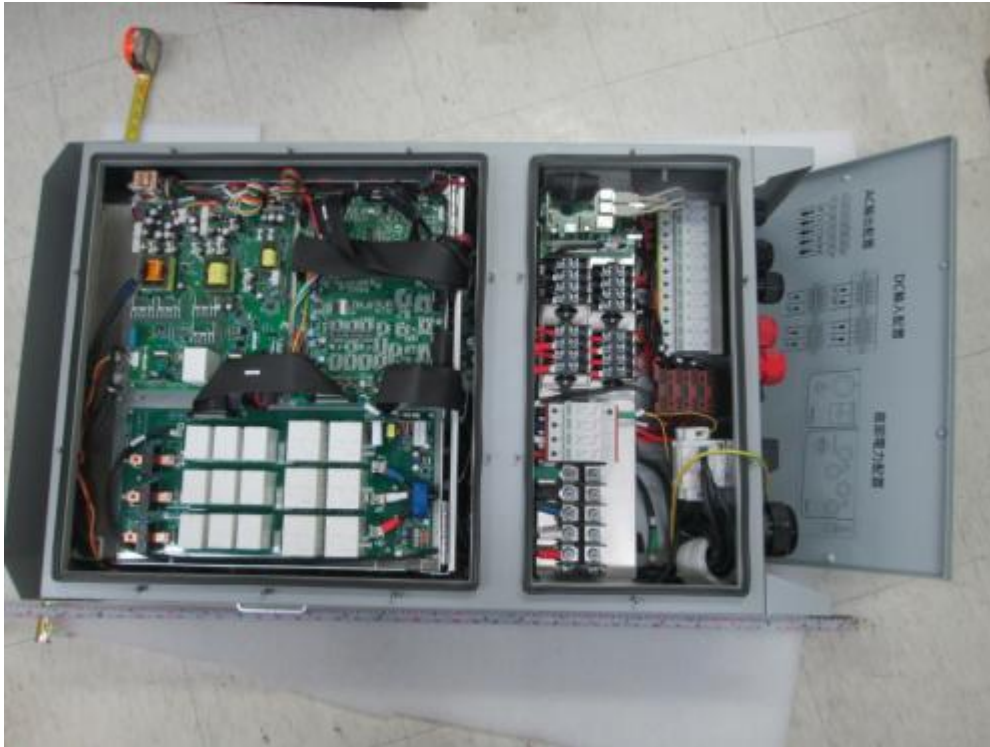
**Enclosure top side**



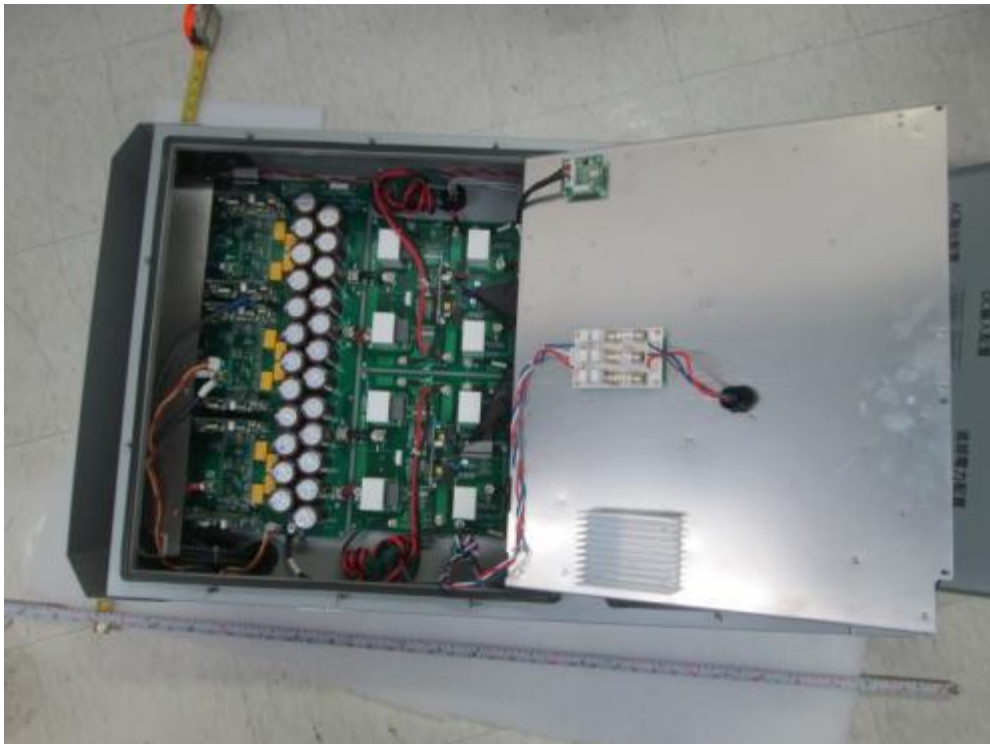
**Enclosure bottom side**



**Internal view-1**



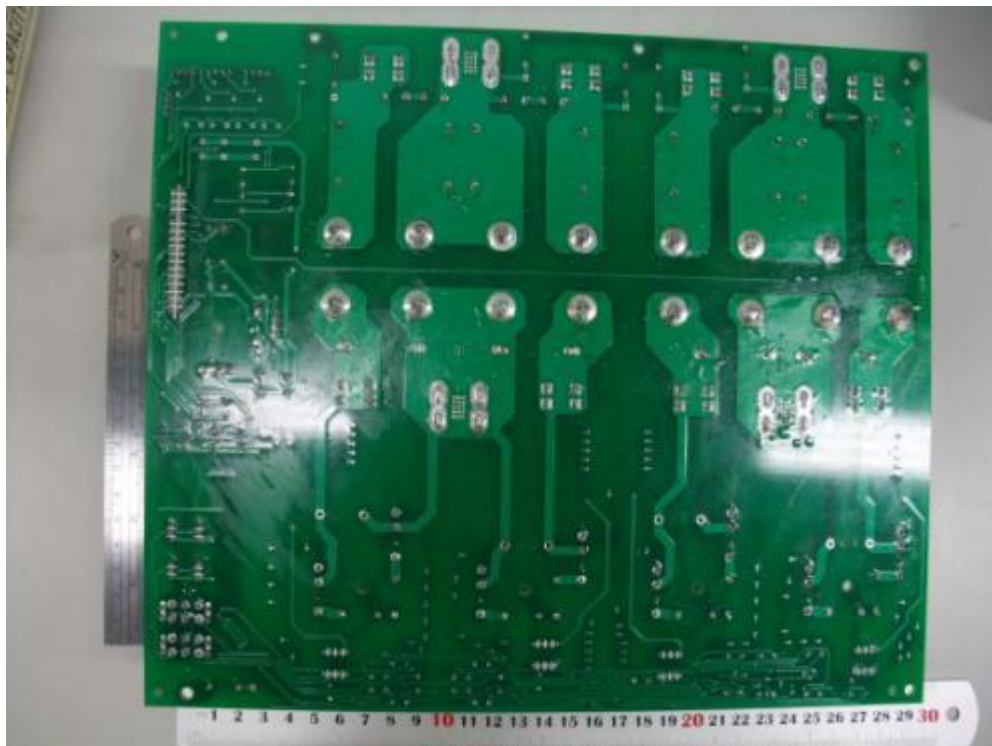
**Internal view-2**



**PCB\_PVLF4 component side**

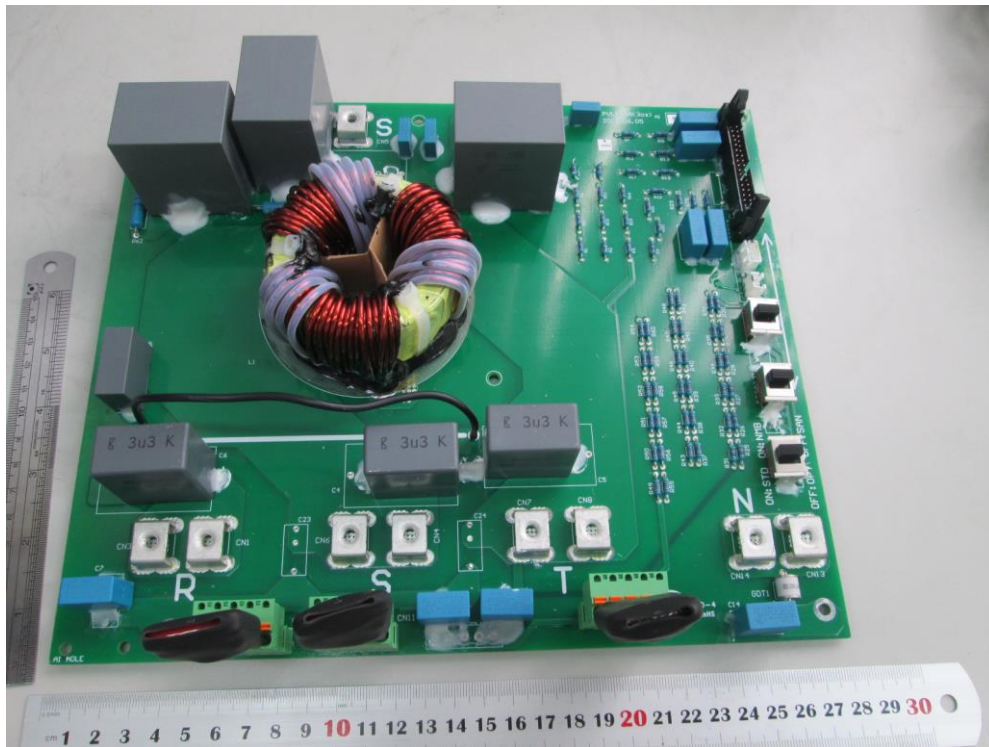


**PCB\_PVLF4 solder side**

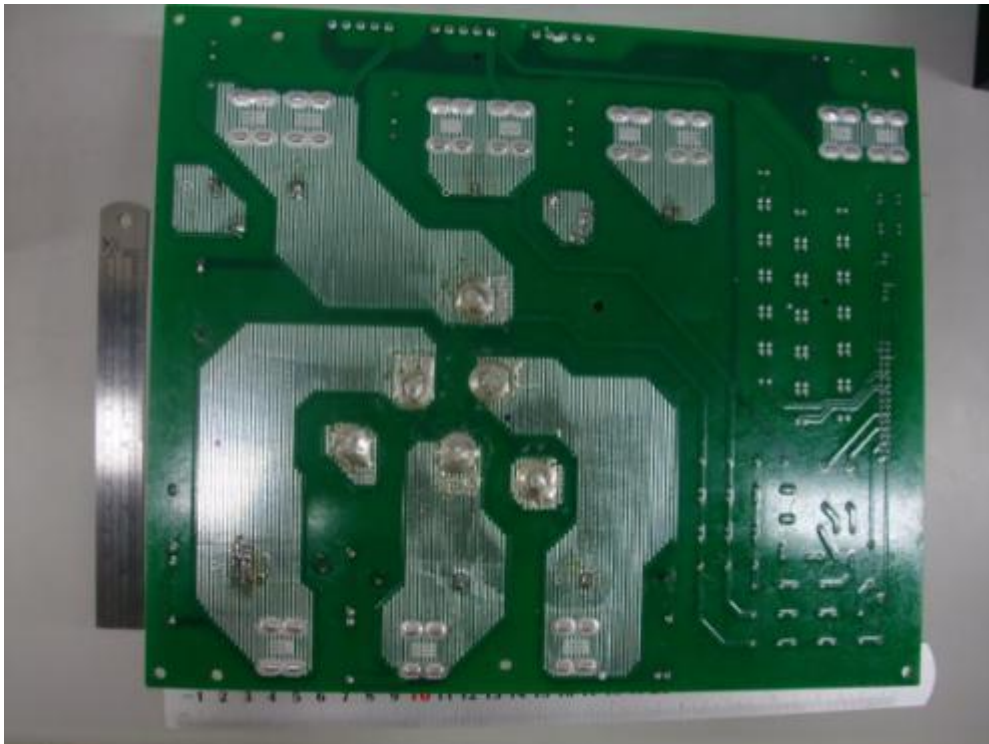




**PCB\_PVLF5 component side**



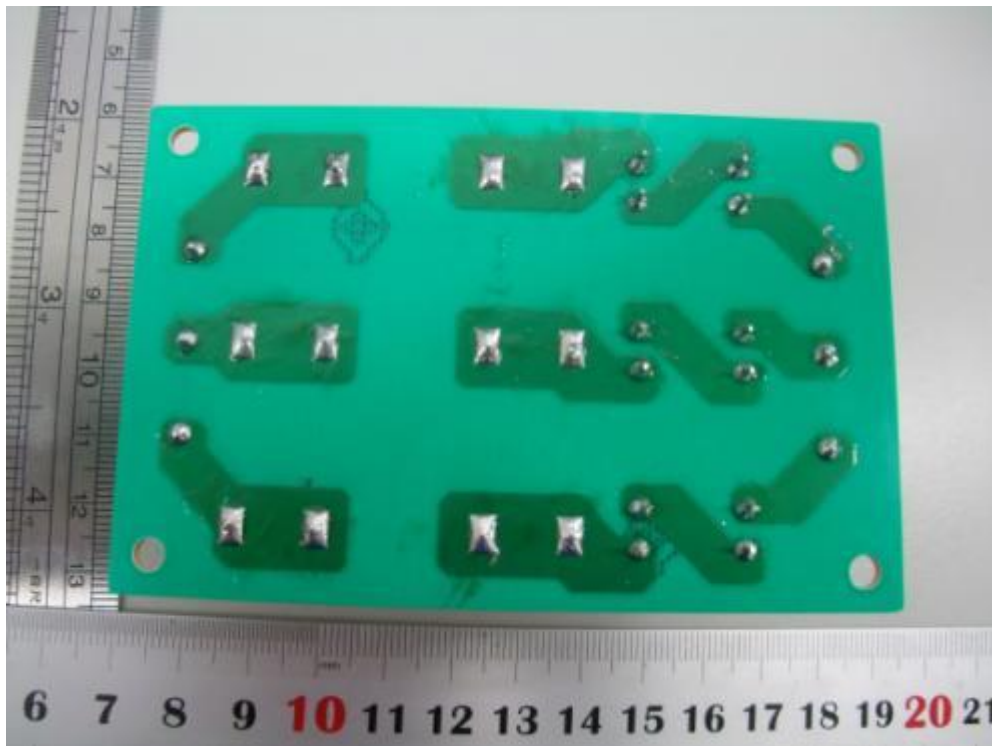
**PCB\_PVLF5 solder side**



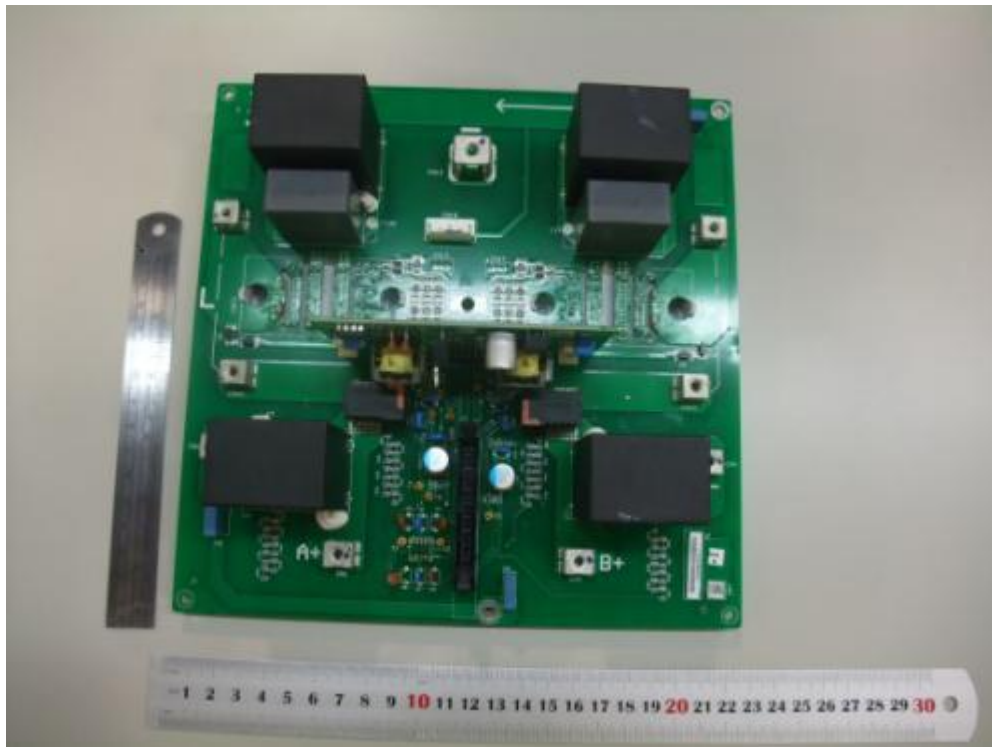
**PCB\_PVLP4 component side**



**PCB\_PVLP4 solder side**



**PCB\_PVLD4 component side**

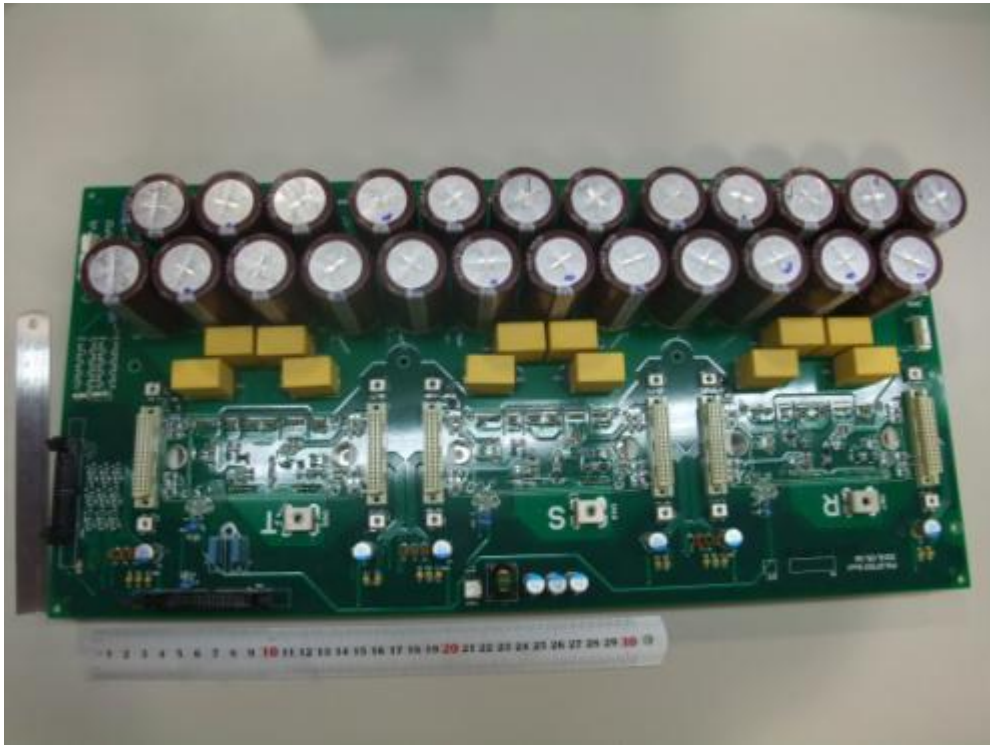


**PCB\_PVLD4 solder side**

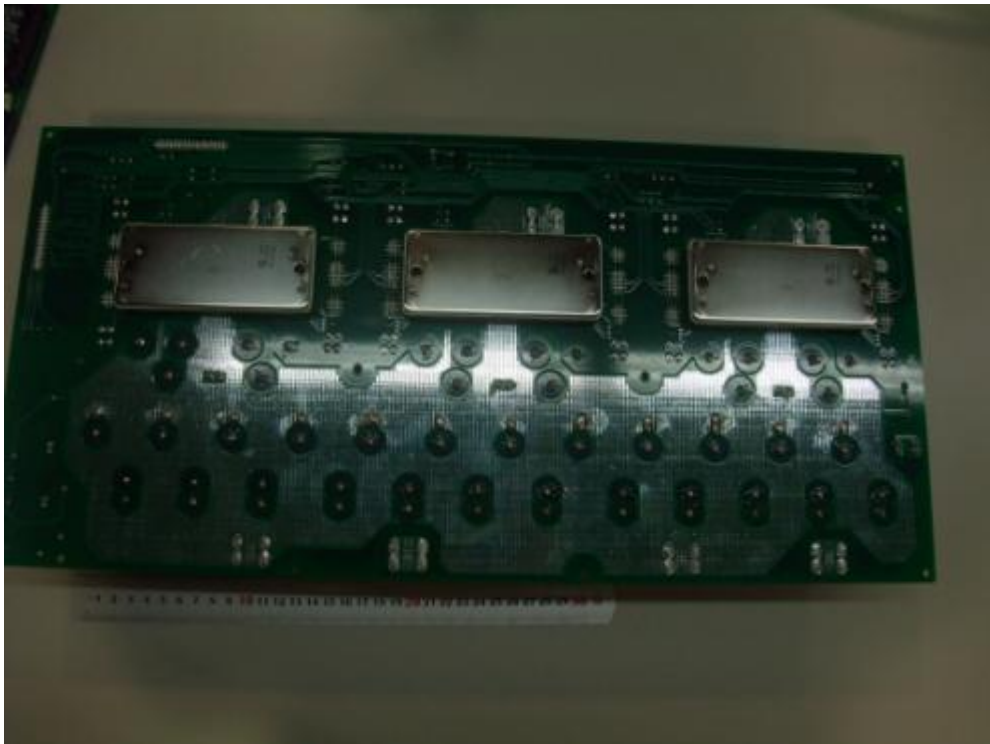




**PCB\_PVLD7 component side**



**PCB\_PVLD7 solder side**

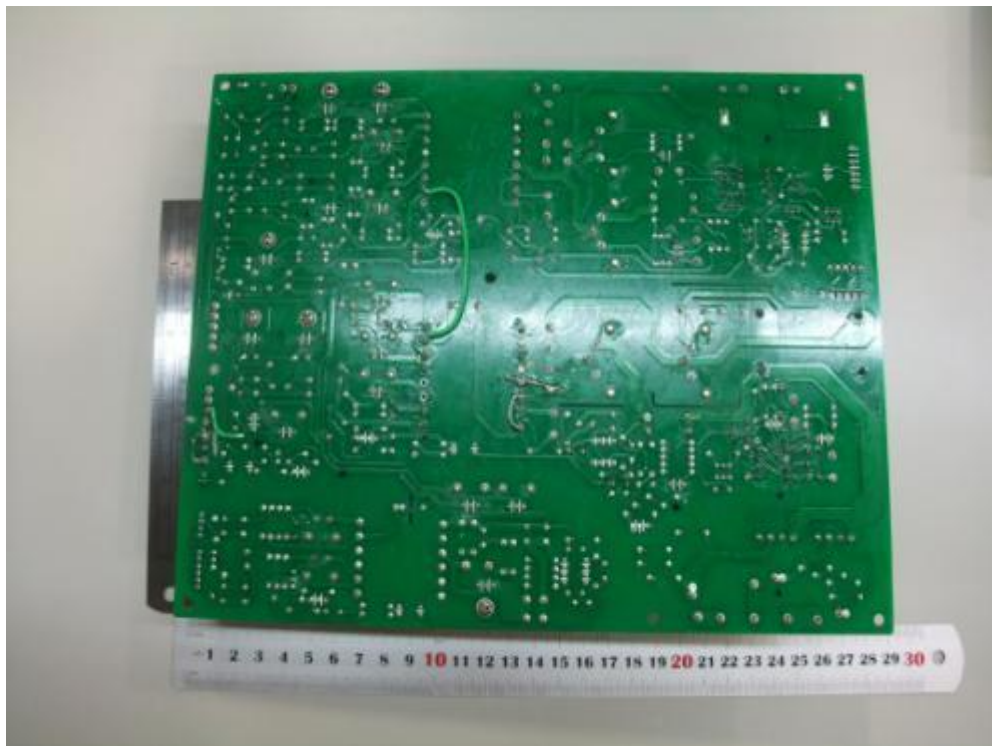




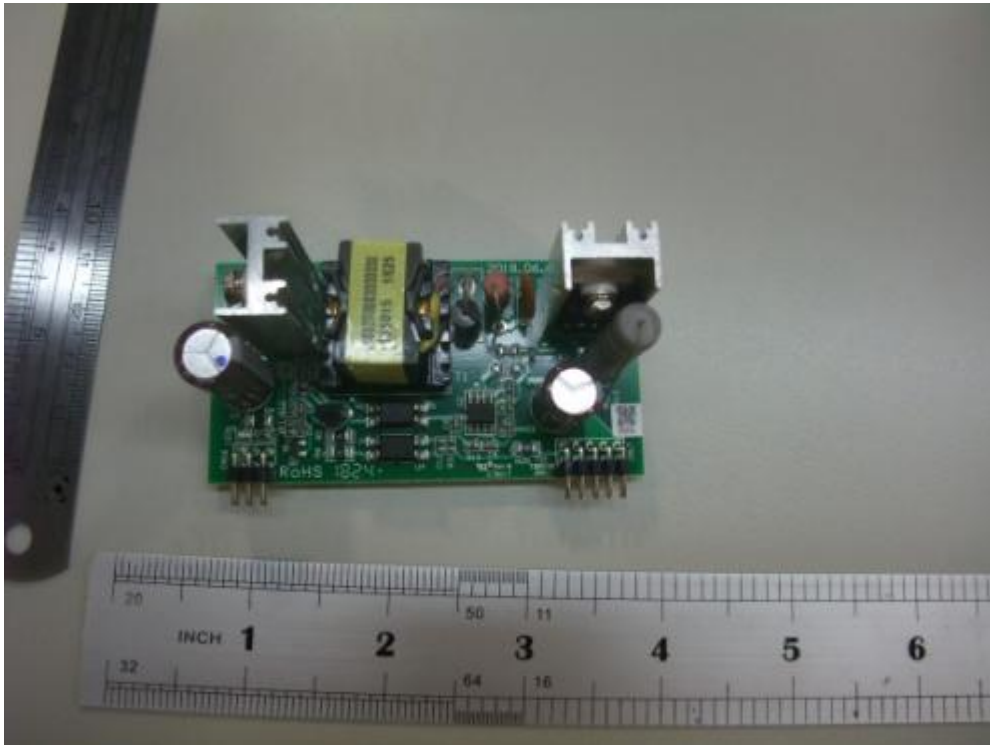
**PCB\_PVLP2 component side**



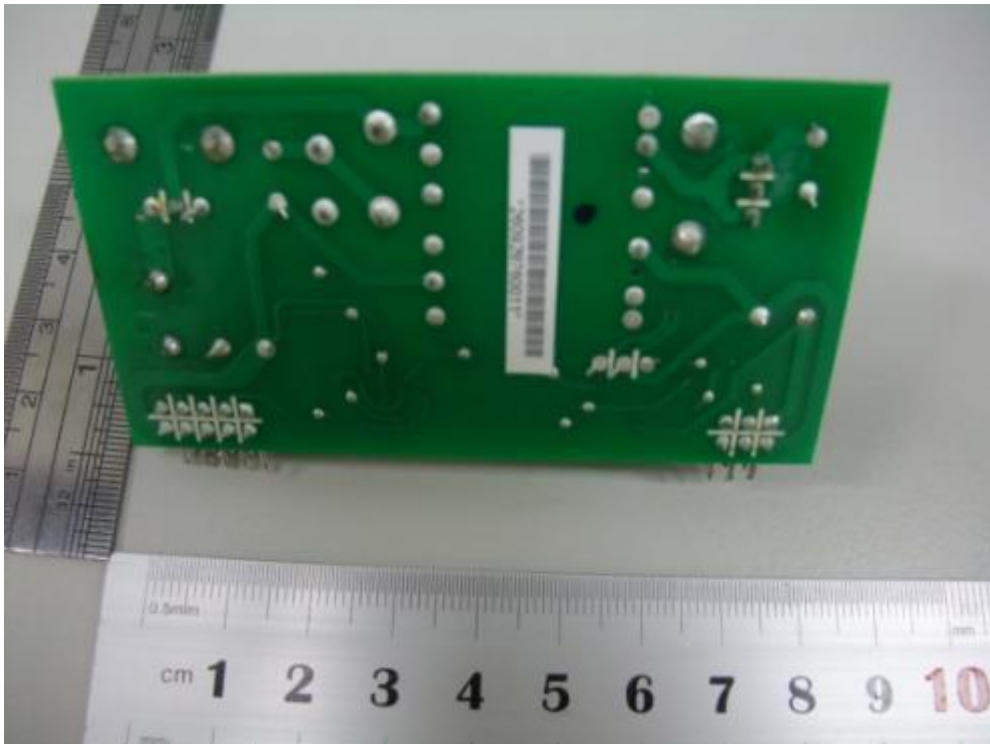
**PCB\_PVLP2 solder side**



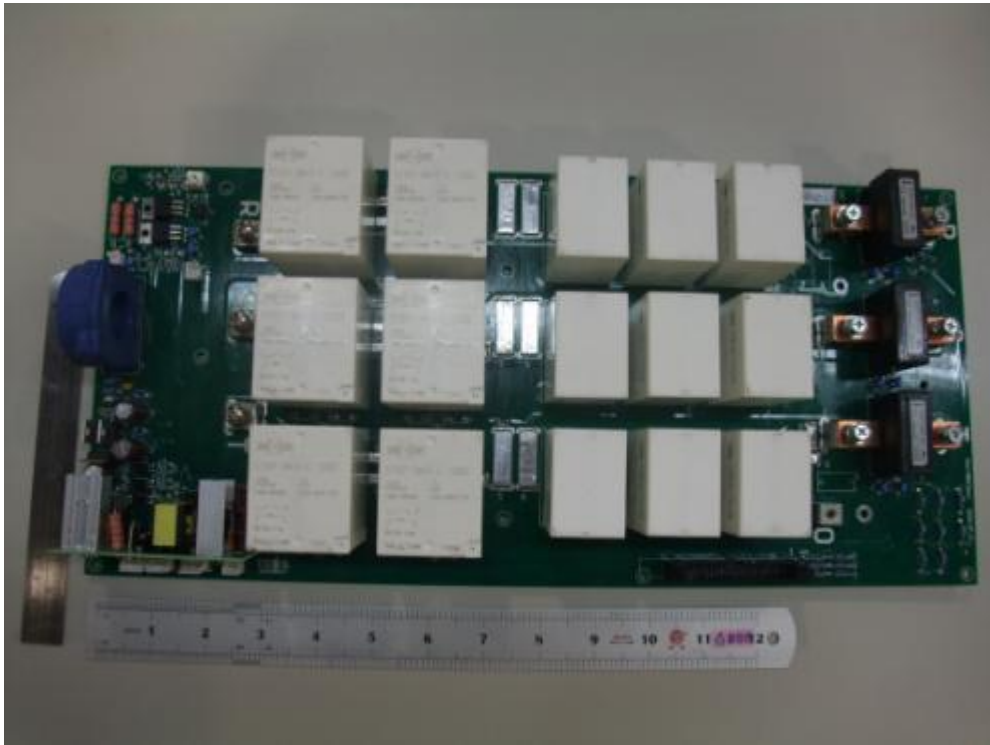
**PCB\_PVLP1 component side**



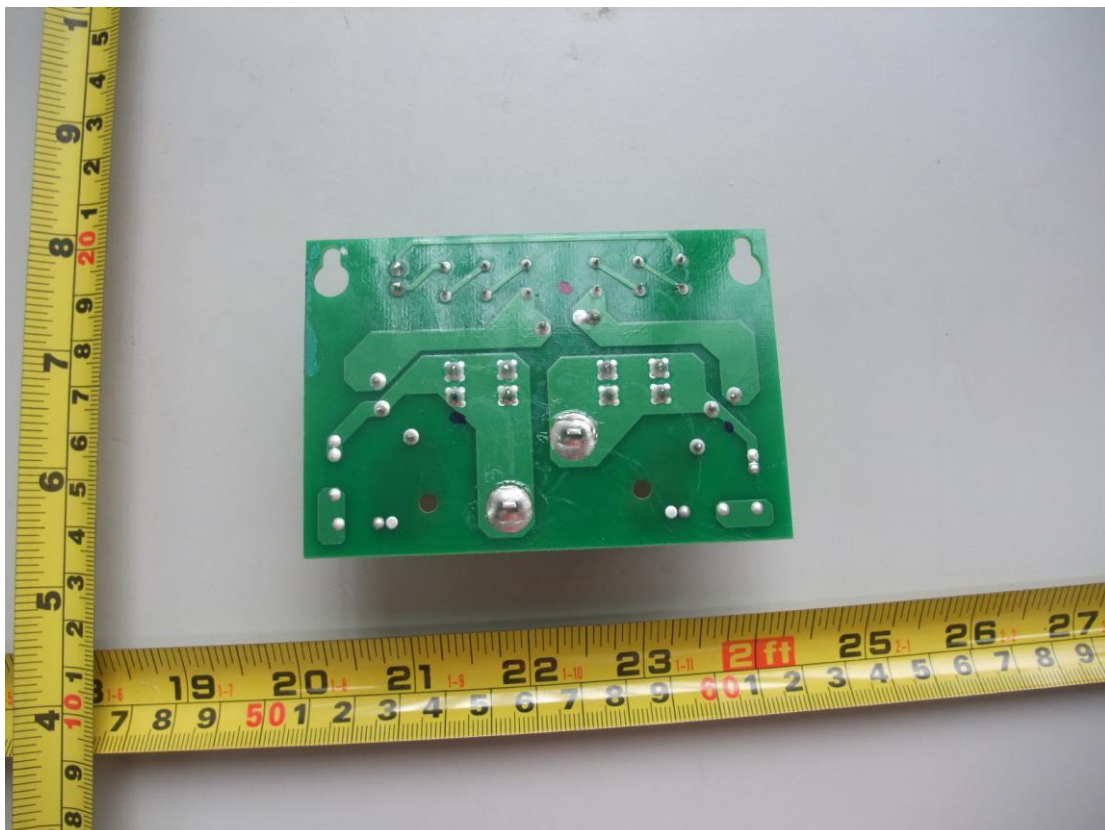
**PCB\_PVLP1 solder side**



**PCB\_PVLF30 component side**



**PCB\_PVLF30 solder side**



## **Annex 2**

### **Test equipment list**

<b>Testing Location:</b>	Bureau Veritas Consumer Products Services (H.K.) Ltd., Taoyuan Branch
<b>Date(s) of performance test:</b>	2018-12-04

Equipment	Manufacturer	Type	Serial No.	Last Calibration
Thermo-Hygro Grapg	Isuzs	3-3127R	30360059	2018-06-13
Precision Power Analyzer	HIOKI	3390	100822876	2018-04-23
Current Transducer	HIOKI	2013	130302927	Combine calibration with HIOKI 3390
Current Transducer	HIOKI	2013	130302926	
Current Transducer	HIOKI	2013	130302925	
Programable DC Source	CHROMA	62150H-1000S	62150EF01541	Monitor by Power Analyzer
Programable DC Source	CHROMA	A620028	620028E00805	
Programable DC Source	CHROMA	62150H-1000S	615120000263	
Programable DC Source	CHROMA	A620028	620028E00801	
Programable DC Source	CHROMA	62150H-1000S	62150EF00857	
Programable DC Source	CHROMA	A620028	620028E00712	
Programable DC Source	CHROMA	62150H-1000S	62150EF01535	
Programable DC Source	CHROMA	A620028	620028E00713	
Programable AC Source	CHROMA	61860	618603800187	Monitor by Power Analyzer
Atmospheric pressure gauge	TESTO	TESTO 511	39108378	2017-06-09